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(71) Applicant (for all designated States except US): **KONIN-
KLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **THEEUWEN,
Stephan, J., C., H.** [NL/NL]; c/o Prof. Holstlaan 6,
NL-5656 AA Eindhoven (NL). **VAN RIJS, Freerk**
[NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven
(NL). **HAMMES, Petra, C., A.** [NL/NL]; c/o Prof.

Holstlaan 6, NL-5656 AA Eindhoven (NL). **POUWEL,
Ivo, B.** [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA
Eindhoven (NL). **JOS, Hendrikus, F., F.** [NL/NL]; c/o
Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

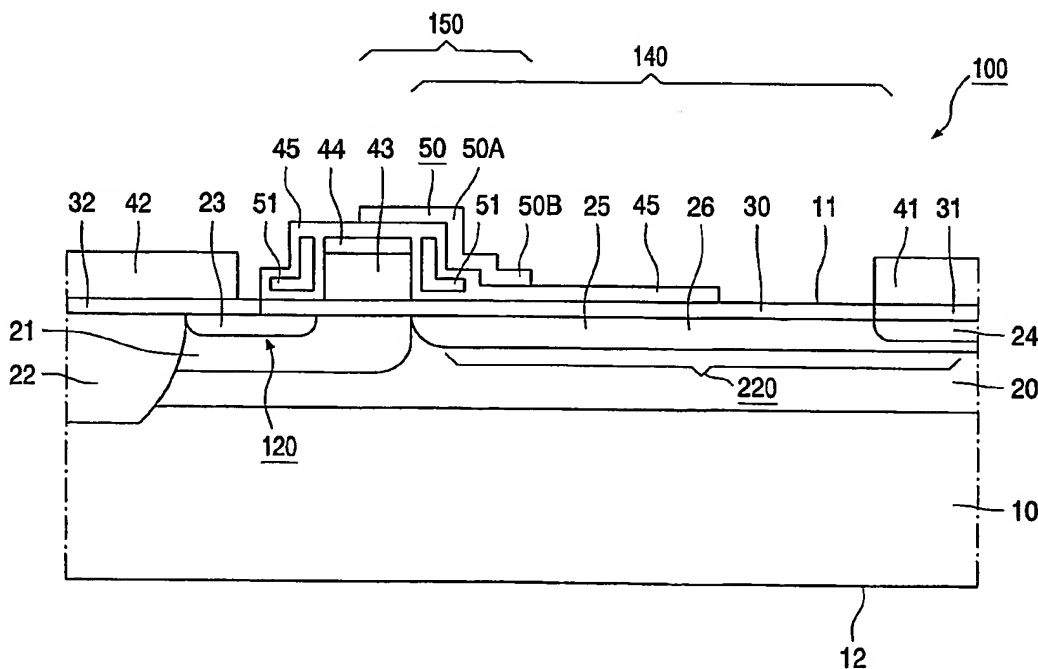
(74) Agent: **ELEVELD, Koop, J.**; Prof. Holstlaan 6, NL-5656
AA Eindhoven (NL).

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(54) Title: ELECTRONIC DEVICE COMPRISING AN LDMOS TRANSISTOR



(57) Abstract: The LDMOS transistor of the invention is provided with a stepped shield structure and/or with a first and a second drain extension region having a higher dopant concentration than the second drain extension region, and being covered by the shield.

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